

















































































**Manual setting of residence IDs Dip switches—IDs list(Expanded system)**

Dip switch	ID	Room#	Name
	A1		
	A2		
	A3		
	A4		
	A5		
	A6		
	A7		
	A8		
	A9		
	A10		
	A11		
	A12		
	A13		
	A14		
	A15		
	A16		
	A17		
	A18		
	A19		
	A20		
	A21		
	A22		
	A23		
	A24		
	A25		
	A26		




























**Manual setting of residence IDs Dip switches—IDs list(Expanded system)**

	A27		
	A28		
	A29		
	A30		
	A31		
	A32		
	A33		
	A34		
	A35		
	A36		
	A37		
	A38		
	A39		
	A40		
	A41		
	A42		
	A43		
	A44		
	A45		
	A46		
	A47		
	A48		
	A49		
	A50		
	A51		
	A52		
	A53		




























**Manual setting of residence IDs Dip switches—IDs list(Expanded system)**

	A54		
	A55		
	A56		
	A57		
	A58		
	A59		
	A60		
	A61		
	A62		
	A63		
	A64		
	A65		
	A66		
	A67		
	A68		
	A69		
	A70		
	A71		
	A72		
	A73		
	A74		
	A75		
	A76		
	A77		
	A78		
	A79		
	A80		




























**Manual setting of residence IDs Dip switches—IDs list(Expanded system)**

	A81		
	A82		
	A83		
	A84		
	A85		
	A86		
	A87		
	A88		
	A89		
	A90		
	A91		
	A92		
	A93		
	A94		
	A95		
	A96		
	A97		
	A98		
	A99		
	A100		
	A101		
	A102		
	A103		
	A104		
	A105		
	A106		
	A107		




























**Manual setting of residence IDs Dip switches—IDs list(Expanded system)**

	A108		
	A109		
	A110		
	A111		
	A112		
	A113		
	A114		
	A115		
	A116		
	A117		
	A118		
	A119		
	A120		
	A121		
	A122		
	A123		
	A124		
	A125		
	A126		
	A127		
	A128		
	A129		
	A130		
	A131		
	A132		
	A133		
	A134		

**Manual setting of residence IDs Dip switches—IDs list(Expanded system)**




























	A135		
	A136		
	A137		
	A138		
	A139		
	A140		
	A141		
	A142		
	A143		
	A144		
	A145		
	A146		
	A147		
	A148		
	A149		
	A150		
	A151		
	A152		
	A153		
	A154		
	A155		
	A156		
	A157		
	A158		
	A159		
	A160		
	A161		

**Manual setting of residence IDs Dip switches—IDs list(Expanded system)**

	A162		
	A163		
	A164		
	A165		
	A166		
	A167		
	A168		
	A169		
	A170		
	A171		
	A172		
	A173		
	A174		
	A175		
	A176		
	A177		
	A178		
	A179		
	A180		
	A181		
	A182		
	A183		
	A184		
	A185		
	A186		
	A187		
	A188		




























appendix2

**Manual setting of residence IDs Dip switches—IDs list(Expanded system)**

























































































































































































































	A189		
	A190		
	A191		
	A192		
	A193		
	A194		
	A195		
	A196		
	A197		
	A198		
	A199		
	A200		
	A201		
	A202		
	A203		
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	A205		
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	A210		
	A211		
	A212		
	A213		
	A214		
	A215		



























































































































































































































**Manual setting of residence IDs Dip switches—IDs list(Expanded system)**

	A216		
	A217		
	A218		
	A219		
	A220		
	A221		
	A222		
	A223		
	A224		
	A225		
	A226		
	A227		
	A228		
	A229		
	A230		
	A231		
	A232		
	A233		
	A234		
	A235		
	A236		
	A237		
	A238		
	A239		
	A240		
	A241		
	A242		




























**Manual setting of residence IDs Dip switches—IDs list(Expanded system)**

       	A243		
       	A244		
       	A245		
       	A246		
       	A247		
       	A248		
       	A249		
       	A250		
       	B251		
       	B252		
       	B253		
       	B254		
       	B255		
       	B256		
       	B257		
       	B258		
       	B259		
       	B260		
       	B261		
       	B262		
       	B263		
       	B264		
       	B265		
       	B266		
       	B267		
       	B268		
       	B269		




























**Manual setting of residence IDs Dip switches—IDs list(Expanded system)**

       	B270		
       	B271		
       	B272		
       	B273		
       	B274		
       	B275		
       	B276		
       	B277		
       	B278		
       	B279		
       	B280		
       	B281		
       	B282		
       	B283		
       	B284		
       	B285		
       	B286		
       	B287		
       	B288		
       	B289		
       	B290		
       	B291		
       	B292		
       	B293		
       	B294		
       	B295		
       	B296		




























**Manual setting of residence IDs Dip switches—IDs list(Expanded system)**

	B297		
	B298		
	B299		
	B300		
	B301		
	B302		
	B303		
	B304		
	B305		
	B306		
	B307		
	B308		
	B309		
	B310		
	B311		
	B312		
	B313		
	B314		
	B315		
	B316		
	B317		
	B318		
	B319		
	B320		
	B321		
	B322		
	B323		

























































































































































































































**Manual setting of residence IDs Dip switches—IDs list(Expanded system)**

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**Manual setting of residence IDs Dip switches—IDs list(Expanded system)**

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**Manual setting of residence IDs Dip switches—IDs list(Expanded system)**




























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**Manual setting of residence IDs Dip switches—IDs list(Expanded system)**




























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**Manual setting of residence IDs Dip switches—IDs list(Expanded system)**

























































































































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**Manual setting of residence IDs Dip switches—IDs list(Expanded system)**

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appendix2

**Manual setting of residence IDs Dip switches—IDs list(Expanded system)**

       	B486		
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